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Predictive Control with Discrete Space-Vector Modulation of Vienna Rectifier for driving PMSG of Wind Turbine Systems

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and Frede Blaabjerg, *Fellow, IEEE*

Abstract—This paper proposes the predictive control with the discrete space-vector modulation (DSVM) for Vienna rectifier connecting to the permanent magnet synchronous generator (PMSG) of the wind turbine system (WTS). Since Vienna rectifier has the special operation principle, Vienna rectifier generates only the feasible 8 voltage vectors, which can be candidate vector for the predictive control, depending on the sign of the input currents. In the proposed predictive control, the feasible voltage vectors are extended from 8 to 19 consisting the 8 original voltage vectors and 11 virtual voltage vectors by using the DSVM for improving the current quality related to the torque ripple, vibration, and noise, and the neutral-point voltage balance with low voltage ripple is guaranteed by using the offset value calculated based on the model of two dc-link capacitors in Vienna rectifier. The scheme for reducing calculation burden is applied in selecting the candidate vector. In addition, the limited operation range for the maximum torque per ampere (MTPA) control of PMSG connected to Vienna rectifier is analyzed. The performance of the proposed predictive control with DSVM for Vienna rectifier with PMSGs is verified in simulation and experiment.

Index Terms—Vienna rectifier, predictive control, wind turbine system, three-level rectifier, permanent magnet synchronous generators.

I. INTRODUCTION

Vienna rectifiers, which are non-generative-boost type, have been extending its applications from the system requiring the power supply [1]-[3] and wind turbine systems (WTS) [4]-[6]. With the application extension of Vienna rectifier, the interest to Vienna rectifier has been increasing in both industry and academia, and the researches on Vienna rectifier have been introduced in lots of field such as topology

[7], control method [1]-[5],[8] and so on [9].

In the researches on the control method, two main characteristics of Vienna rectifier have been considered. First one is the special operation condition: the sign of the input voltage of Vienna rectifier should be the same as that of corresponding input current and they should be applied in three phases [1],[10]. Second one is that Vienna rectifier has two dc-link capacitors like three-level topologies [11]. Owing to it, the control method used in Vienna rectifier should take not only current quality but also the balance of two dc-link capacitors into account [9].

Several representative control methods are introduced as follows: Control method based on the hysteresis was proposed at the beginning of the research [1]. Then, the control methods using the carrier-based pulse-width modulation (CB-PWM) or space-vector modulation (SVM) have been proposed [3],[9]-[10],[12]. In these methods, the proportional-integral (PI) controls, which is already validated in other AC/DC topologies, are used with the transformation principle [9]-[10],[12]. Recently, the model predictive control (MPC) methods for Vienna rectifier are proposed [4]-[6],[13].

The MPC approach has been applied in various power electronics applications. In AC/DC and DC/AC converters especially, the similar MPC approach can be used in various topologies by considering the operation characteristic of its topology. Depending on the topology, however, the control parameters can be different and it should be considered in applying the MPC approach. The simple topology is two-level topology. It has only AC currents as the control parameter in the cost function if the DC-link voltage is fixed (one converter of back-to-back converter used in WTS or motor drive systems).

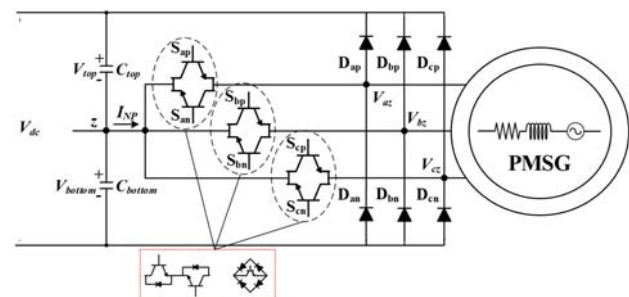


Fig. 1. Vienna rectifier connected to PMSG in the wind turbine systems.

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Therefore, the MPC method used in two-level topology is simple and very intuitive. In the three-level topologies, not only the AC currents but also the neutral-point voltage should be control parameters and they are considered in cost function or vector selection process of MPC [14]-[15]. In addition to the essential control parameters mentioned above, the user requirement such as the number of switching to reduce the switch loss can be added to the control parameter [4].

To improve the performance of MPC in terms of current ripple (or torque ripple), the discrete space vector modulation (DSVM) concept is applied in lots of papers [17]-[20]. A lot of virtual voltage vectors are created by using the DSVM and each virtual voltage vector consists of the several real voltage vectors represented by the switching states. It means that more than two voltage vectors exist in one switching period; therefore, it leads to the current ripple reduction.

The MPC approach has been proposed for Vienna rectifiers [4]-[6],[13]. The conventional FCS-MPC concept is applied to Vienna rectifier in [4] and the feasible 8 vectors on each sector are used. In the process to determine the optimized vector, the neutral-point voltage balance is considered at first, it results in the candidate vectors for applying the FCS-MPC with the cost function consisting current ripples and the number of switching. In [5], the direct torque control for Vienna rectifier considers not only the torque and flux, but also the neutral-point voltage and then the optimized voltage vector generates by the look-up table. In a finite control set-MPC (FCS-MPC) method for Vienna rectifier [6], the neutral-point voltage control has high priority; therefore, the voltage vectors satisfying the defined tolerant value of the neutral-point voltage are selected as candidate vector. Both the neutral-point voltage and current are considered in the cost function of FCS-MPC [13]. The FCS-MPC with DSVM has proposed in [16] and the nineteen voltage vectors become the candidate vector for applying the cost function consisting of the current ripples. Then, the value of neutral-point voltage determines what the optimized voltage vector generates.

This paper is based on the FCS-MPC with DSVM for Vienna rectifier connecting to the permanent magnet synchronous generator (PMSG) of WTS and its configuration is shown in Fig. 1. The proposed FCS-MPC with DSVM, at first, draws the candidate vectors by the proposed voltage vector selection principle; therefore, the calculation burden reduction, which is main disadvantage of FCS-MPC with DSVM, is achieved. Secondly these are used to determine the optimized voltage vector minimizing the cost function consisting the current ripples. Finally, the offset voltage to maintain the neutral-point voltage balance is calculated based on the neutral-point voltage model of Vienna rectifier and is applied to final reference voltages for DSVM because the DSVM enable the offset voltage to be applied. In all process, Vienna rectifier operation requirement is considered as constraints. In addition, the limited operation range for the maximum torque per ampere (MTPA) control of PMSG connected to Vienna rectifier is analyzed.

II. MODEL OF PMSG AND VIENNA RECTIFIER

In the proposed FCS-MPC, two models are considered: PMSG model connected to Vienna rectifier and neutral-point voltage model of Vienna rectifier. In this chapter, two models are defined and the operation principle for Vienna rectifier is introduced briefly.

A. PMSG model connected to Vienna rectifier

Vienna rectifier can be considered as voltage source, therefore, the PMSG model used in voltage source inverters [4]-[5],[21] is considered in this paper and it is represented as

$$\begin{aligned} V_{de} &= -R_s i_{de} - L_{ds} \frac{di_{de}}{dt} + \omega_s L_{qs} i_{qe} \\ V_{qe} &= -R_s i_{qe} - L_{qs} \frac{di_{qe}}{dt} - \omega_s L_{ds} i_{de} + \omega_s \lambda_m \end{aligned} \quad (1)$$

where V_{de} , V_{qe} , i_{de} , i_{qe} are d-axis voltage, q-axis voltage, d-axis current, and q-axis current of Vienna rectifier in the synchronous reference frame, R_s , L_d , and L_q are the stator resistance, d-axis inductance, and q-axis inductance of the PMSG, ω_s is the angular speed of stator, and λ_m is the flux induced by the magnet, respectively.

Equation (1) is transformed to the discrete model with a sampling period T_s . From the discrete model, the d-axis and q-axis currents of Vienna rectifier are predicted as

$$\begin{aligned} i_{de}[k+1] &= \frac{T_s (\omega_s L_{qs} i_{qe}[k] - V_{de}[k])}{L_{ds}} + \left(1 - T_s \frac{R_s}{L_{ds}}\right) i_{de}[k] \\ i_{qe}[k+1] &= \frac{T_s (\omega_s \lambda_m - \omega_s L_{ds} i_{de}[k] - V_{qe}[k])}{L_{qs}} + \left(1 - T_s \frac{R_s}{L_{qs}}\right) i_{qe}[k] \end{aligned} \quad (2)$$

where ω_s is variable value depending on time; however, the proposed FCS-MPC assumes that $\omega_s[k] = \omega_s[k+1]$ because the speed is not varied rapidly for T_s in WTS. In case of the surface-mounted PMSG (SPMSG), both L_d and L_q can be substituted to L_s in (1) and (2)

B. Neutral-point voltage model of Vienna rectifier

The neutral-point voltage (V_{NP}) means the difference between the top capacitor voltage (V_{top}) and bottom capacitor voltage (V_{bottom}) of dc-link. V_{NP} is influenced by the neutral-point current (I_{NP}). The neutral-point voltage model of Vienna rectifier is the same as that of three-level topology; therefore, the equation in [9],[11] can be applied for the average I_{NP} calculation during one switching period (T_{sw}), which is T_s in the proposed FCS-MPC, and it is represented as

$$I_{NP} = -(V_{max} \cdot I_{max} + V_{mid} \cdot I_{mid} + V_{min} \cdot I_{min}), \quad (3)$$

where V_{max} , V_{mid} , and V_{min} are maximum, medium, and minimum voltages of V_a , V_b and V_c , respectively, and I_{max} , I_{mid} , and I_{min} are the currents of V_{max} , V_{mid} , and V_{min} , respectively. V_a , V_b and V_c are transformed to V_{qe} and V_{de} of the synchronous reference frame by the coordinates transformation [10],[13].

The discrete equation for neutral-point voltage model of three-level topology in [11] shows the relationship between V_{NP}

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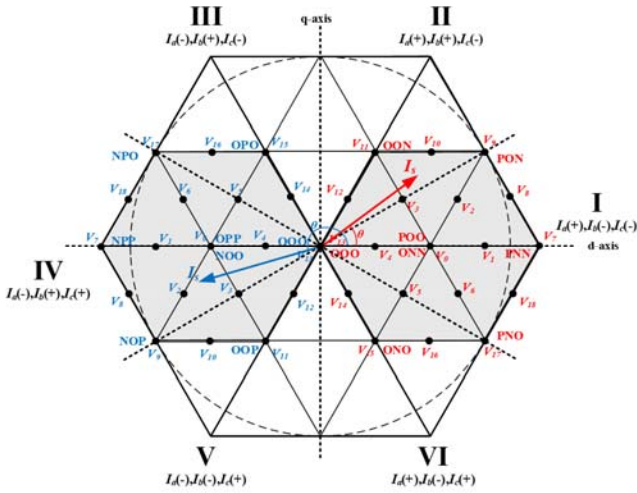


Fig. 2. The DSVM based voltage vector diagram used in the proposed FCS-MPC.

and I_{NP} and it is represented as

$$\begin{aligned} V_{NP}[k+1] &= V_{top}[k+1] - V_{bottom}[k+1] \\ &= V_{NP}[k] + \frac{1}{C} I_{NP}[k+1] T_s \\ &= V_{NP}[k] \\ &\quad + \frac{2T_s}{V_{dc}C} (|V_{max}(k+1)| \cdot I_{max}(k+1) \\ &\quad + |V_{mid}(k+1)| \cdot I_{mid}(k+1) \\ &\quad + |V_{min}(k+1)| \cdot I_{min}(k+1)) \end{aligned} \quad (4)$$

where C is the capacitance of dc-link.

C. Vienna rectifier operation

Vienna rectifier using the bidirectional switch of Fig. 1 is considered in this paper. There are three switching states (P, O, and N). The P-switching state indicates $S_{xp}(ON)$ and $S_{xn}(OFF)$ and N-switching state means $S_{xp}(OFF)$ and $S_{xn}(ON)$. The O-switching state occurs by $S_{xp}(ON)$ and $S_{xn}(ON)$. Each leg has the switching state of P, O, and N, and the combination of three legs are expressed as the voltage vectors shown in Fig. 2.

Vienna rectifier has the requirement for regulating the sinusoidal currents. The polarity of a leg's voltage of Vienna rectifier should be the same as the polarity of a corresponding leg's current [1],[10]. Therefore, depending on polarity of the three currents, the voltage vectors for applying to the FCS-MPC should be limited as shown in Fig. 2. Therefore, the first step for the proposed FCS-MPC is to find out Sector containing the current vector (I_s). Sector can be determined by using the three leg currents and it is summarized in Table I.

III. THE PROPOSED FCS-MPC WITH DSVM FOR VIENNA RECTIFIER

All steps of the proposed FCS-MPC with DSVM are shown in Fig. 3. There are four steps: Step I. Voltage vector selection, Step II. Current control, Step III. Neutral-point voltage control, and Step IV. Reference voltage decision. The proposed FCS-MPC with DSVM for Vienna rectifier focuses on two

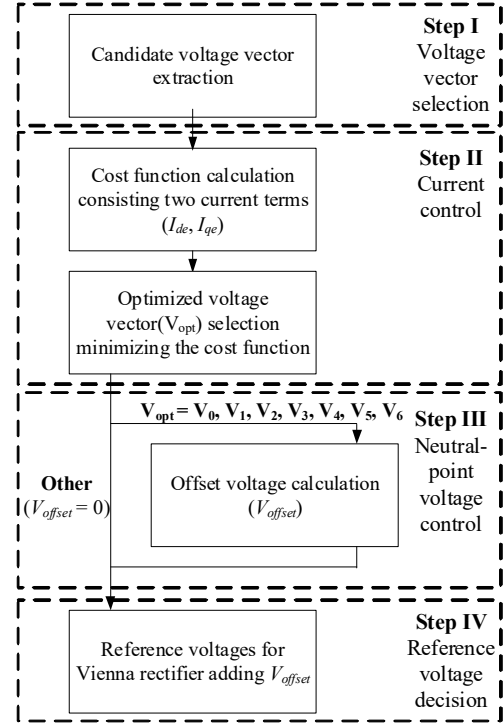


Fig. 3. Flowchart of the proposed FCS-MPC with DSVM.

Sector	Current	Sector	Current
I	$i_a(+), i_b(-), i_c(-)$	IV	$i_a(-), i_b(+), i_c(+)$
II	$i_a(+), i_b(+), i_c(-)$	V	$i_a(-), i_b(-), i_c(+)$
III	$i_a(-), i_b(+), i_c(-)$	VI	$i_a(+), i_b(-), i_c(+)$

control targets: the input current and neutral-point voltage of Vienna rectifier. In this chapter, each step is explained in order.

A. Step I: Voltage vector selection

The conventional MPC just has the 8 voltage vectors per each Sector in Vienna rectifier [4][13]. However, there are totally 19 voltage vectors per each Sector in Fig. 2. It is because the DSVM allows the voltage vectors to be divided. It means that the current quality can be improved. Nevertheless, the DSVM aggravates the computation burden in applying the FCS-MPC owing to increasing the number of candidate vectors. Therefore, this paper suggests the method for extracting the effective candidate voltage to reduce the computation burden.

The voltage vector selection method is established to achieve the minimization of the current ripple in the current control. To reduce the current ripple, the voltage vectors closed to the desired voltage vector (V_{ref}) generating the desired current (I_s) should be selected. In step of the voltage vector section, the magnitude of V_{ref} is used and $|V_{ref}|$ is calculated from (1) as

$$|V_{ref}| = \sqrt{\left(-R_s i_{de,ref} - L_{ds} \frac{di_{de,ref}}{dt} + \omega_s L_{qs} i_{qe,ref} \right)^2 + \left(-R_s i_{qe,ref} - L_{qs} \frac{di_{qe,ref}}{dt} - \omega_s L_{ds} i_{de,ref} + \omega_s \lambda_m \right)^2} \quad (5)$$

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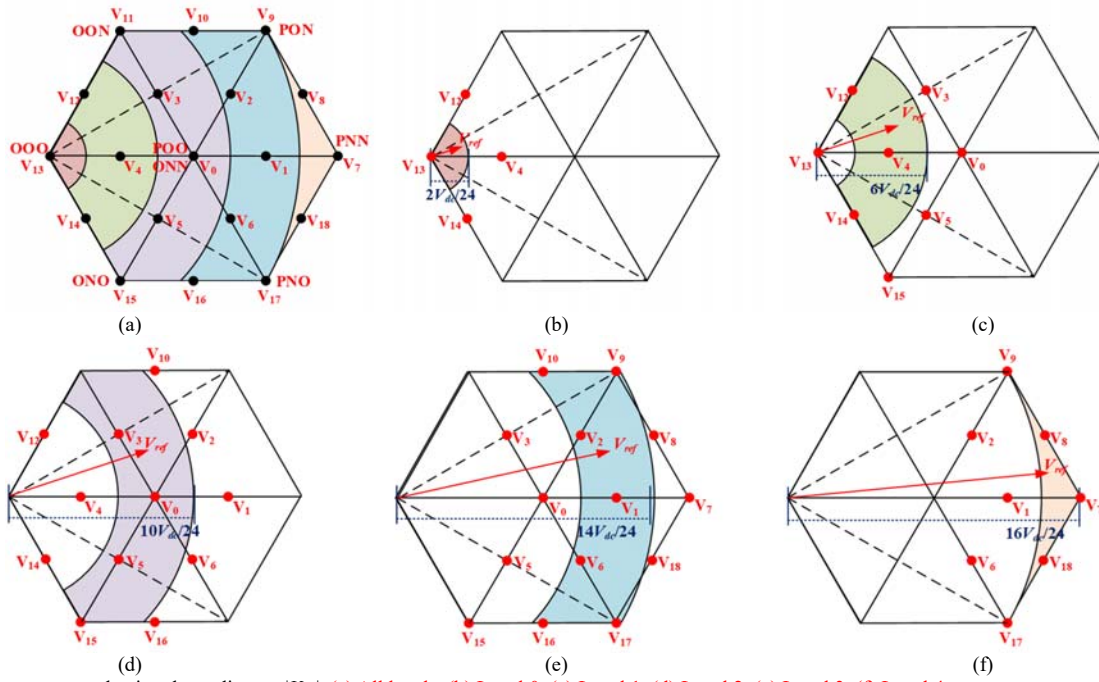


Fig. 4. Voltage vector selection depending on $|V_{ref}|$. (a) All levels. (b) Level 0. (c) Level 1. (d) Level 2. (e) Level 3. (f) Level 4.

TABLE II
VOLTAGE VECTOR SELECTION DEPENDING ON $|V_{ref}|$

Level	$ V_{ref} $	Candidate vectors	Number of candidate vectors
0	$0 < V_{ref} \leq 2V_{dc}/24$	$V_4, V_{12}, V_{13}, V_{14}$	4
1	$2V_{dc}/24 < V_{ref} \leq 6V_{dc}/24$	$V_0, V_3, V_4, V_5, V_{12}, V_{13}, V_{14}, V_{15}$	8
2	$6V_{dc}/24 < V_{ref} \leq 10V_{dc}/24$	$V_0, V_1, V_2, V_3, V_4, V_5, V_6, V_{10}, V_{12}, V_{14}, V_{15}, V_{16}$	12
3	$10V_{dc}/24 < V_{ref} \leq 14V_{dc}/24$	$V_0, V_1, V_2, V_3, V_5, V_6, V_7, V_8, V_9, V_{10}, V_{15}, V_{16}, V_{17}, V_{18}$	14
4	$14V_{dc}/24 < V_{ref} \leq 16V_{dc}/24$	$V_1, V_2, V_6, V_7, V_8, V_9, V_{17}, V_{18}$	8

where $i_{de,ref}$ is d-axis reference current indicating the flux of PMSG and $i_{qe,ref}$ is q-axis reference current indicating the torque of PMSG. The dominant component of $|V_{ref}|$ is $\omega_s \lambda_m$ which is proportional to the speed of the PMSG and the deferential terms in (5) exists only during the transient state where the one or two of $i_{de,ref}$ and $i_{qe,ref}$ are changed.

Based on the voltage vector diagram of Fig. 2, $|V_{ref}|$ belongs to one of five levels as shown in Fig. 4(a). Depending on $|V_{ref}|$, the voltage vectors are extracted as the candidate vector. As the example, the $V_0, V_3, V_4, V_5, V_{12}, V_{13}, V_{14}$, and V_{15} are selected as the candidate vector for applying the cost function where $|V_{ref}|$ is between $2V_{dc}/24$ and $6V_{dc}/24$, and it is Level 1 as shown in Fig. 4(c). All cases of Fig. 4(b) – (f) are summarized in Table II.

Two voltage vectors V_{11} and V_{15} are placed on other Sector; therefore, it is not necessary to consider two voltage vectors. However, V_{15} is contained in Fig. 4(c) – (e). In the normal operation of converter with the PMSG, the converter voltage and the counter electromotive force (V_{EMF}) of PMSG have the phase difference to generate the current flow [22] and the current angle is matched at (unity power factor) or move with respect to the angle of V_{EMF} (lagging current or leading current condition). Therefore, V_{11} and V_{15} can be used because of the phase difference between the current and V_{EMF} . Since the rectifier has only the leading current with respect to V_{EMF} ;

however, only V_{15} is considered in this paper.

B. Step II: Current Control (FCS-MPC)

Step 2 is the current control. The FCS-MPC is used as the current controller to improve current quality extremely. The neutral-point voltage can be controlled by the injection of offset voltage because of the DSVM used in this paper. In the current control, the d-axis current (i_{de}) and q-axis current (i_{qe}) of Vienna rectifier in the synchronous reference frame are used as the control variable. $i_{de}[k+1]$ and $i_{qe}[k+1]$ depending on the voltage vector (V_i) are estimated from (2) and they are expressed as

$$(i_{de}[k+1][V_i], i_{qe}[k+1][V_i]), \quad i = (\text{Table II}). \quad (6)$$

Although the various constructions of the cost function (CF) are showing in lots of papers [4]-[6],[14]-[15], only $i_{de}[k+1][V_i]$ and $i_{qe}[k+1][V_i]$ are applied to calculate CF. Therefore, CF is expressed as

$$CF = (i_{de,ref} - i_{de}[k+1][V_i])^2 + (i_{qe,ref} - i_{qe}[k+1][V_i])^2, \quad i = (\text{Table II}). \quad (7)$$

Then V_i minimizing (7) is selected as the optimized voltage vector (V_{opt}) and it is expressed as

$$V_{opt} \leftarrow \min_{i=\text{Table II}} CF[V_i]. \quad (8)$$

C. Step III: Neutral-Point Voltage Control

The proposed control is based on the DSVM. It means that

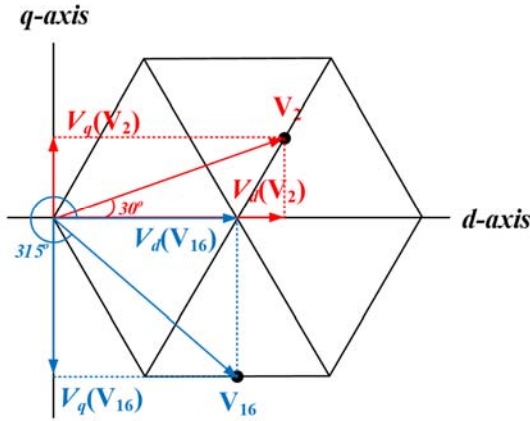


Fig. 5. The calculation of d-axis value (V_d) and q-axis value (V_q) of V_i .

the switching state cannot be fixed in one switching period. Therefore, the offset voltage (V_{offset}) can be used to change three reference voltages (V_a , V_b , V_c). Adding V_{offset} to V_a , V_b , and V_c influences on the variation of V_{NP} [9]-[10]. The adjustable V_{offset} keeps V_{NP} to zero with the maintenance of the input current quality.

To apply V_{offset} , V_i should be represented to V_a , V_b , and V_c . In first, d-axis value (V_d) and q-axis value (V_q) of V_i are calculated from the space vector diagram of Fig. 5. In this transformation, the degree (θ_{dq}) between V_i and the d-axis which is expressed with the stator angle (θ_s) of the PMSG, and the magnitude ($|V_i|$) of V_i are needed to calculate $V_d[V_i]$ and $V_q[V_i]$ and they are expressed as

$$\begin{aligned} V_d[V_i] &= |V_i| \cos \theta_{dq} \\ V_q[V_i] &= |V_i| \sin \theta_{dq} \end{aligned} \quad , \theta_{dq} = \theta_s + \frac{\pi}{2} \quad (9)$$

Then, the result of (9) is represented to V_a , V_b , and V_c thought the abc-dq transformation and they are expressed as

$$\begin{aligned} V_a[V_i] &= V_d[V_i] \\ V_b[V_i] &= -\frac{1}{2}V_d[V_i] + \frac{\sqrt{3}}{2}V_q[V_i] \\ V_c[V_i] &= -\frac{1}{2}V_d[V_i] - \frac{\sqrt{3}}{2}V_q[V_i] \end{aligned} \quad (10)$$

All V_i cannot reflect V_{offset} . To define the feasible V_i permitting that V_{offset} can be added, the nineteen V_i are classified to two groups: Group I is [V_8 , ..., V_{18}] and Group II is [V_0 , ..., V_6]. Group I means that it is not possible to add V_{offset} to their voltage vectors. Fig. 6 shows the switching sequence of the representative voltage vectors in Group I when current vector is located in Sector I. V_{12} is configured by the two voltage vectors (V_{11} and V_{13}) near itself and the V_{NP} is changed by only V_{11} . This configuration is shown in V_8 , V_{10} , V_{14} , V_{16} , and V_{18} , similarly. V_{NP} is effected by V_9 , V_{11} , V_{15} , and V_{17} who are defined as the small vector or medium vector. In addition, adding V_{offset} violates the rule of Vienna rectifier operation. In the case of the positive V_{offset} , the switching sequence is changed as blue dot-line of Fig. 6(a) and the polarity (+) of the changed V_{bz} is not the same as $I_b(-)$. The negative V_{offset} leads to the red dot-line of Fig. 6(a) where the violated situation in a-leg. Consequently, V_{NP} is not controllable if the voltage vector of

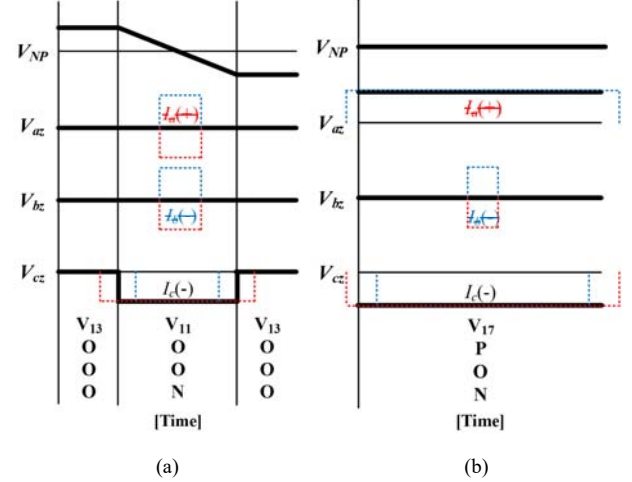


Fig. 6. V_{NP} and switching sequence of voltage vectors of Group I in Sector I. (a) V_{12} . (b) V_{17} .

the Group I is determined as the optimized voltage vector. Fig. 6(b) shows the case of V_{17} consisting of only itself like V_9 , V_{11} , V_{13} , V_{15} , and V_{17} . Similar to V_{12} , the violated situation occurs when V_{offset} is added.

Group II consists of the voltage vectors what can change V_{NP} by adding the V_{offset} . The characteristic is similar to that of three-level topologies [11], [14]. Since Vienna rectifier has the operation constrain, the completed freedom in applying V_{offset} is not assigned in the neutral-point voltage control.

The range of V_{offset} is determined from the switching sequence analysis. The switching sequence of V_4 in Sector I has the two voltage vectors (V_0 , V_{13}) but there are three switching states ([OOO], [POO], [ONN]) as shown in Fig. 7(a). [POO] and [ONN] have the same equivalent three-leg output voltage but influence on V_{NP} in the opposite side. Depending on how to add V_{offset} to V_4 , although the three-leg output voltages are fixed as desired value, V_{NP} can be changed. Fig. 7(b) shows the waveform changed by the positive and negative V_{offset} s. When the positive V_{offset} raises up V_{NP} as the line of Fig. 7(b) and it increases the ON-time (T_{POO}) of [POO] decreases the ON-time (T_{ONN}) of [ONN]. The negative V_{offset} leads to the decrement of V_{NP} . V_{offset} can be added until the ON-time of [POO] or [ONN] becomes zero. Therefore, the range of V_{offset} for V_4 is related to T_{ONN} and T_{POO} . The T_{ONN} and T_{POO} in V_4 are the same as $|T_{min}|$ and $|T_{max}|$ respectively. They are defined by using three-leg ON-times ($|T_a|$, $|T_b|$ and $|T_c|$): T_{max} and T_{min} are the maximum and minimum values of T_a , T_b and T_c calculated from V_a , V_b , V_c , respectively. The relationship between T_a , T_b , T_c and V_a , V_b , V_c is represented as

$$T_x = \frac{2V_x}{V_{dc}} T_s \quad x = a, b, c, \quad (11)$$

where T_s is a switching period. If T_x is negative, the negative voltage ($-V_{dc}/2$) is generated and the ON-time is $|T_x|$ as shown in V_{bz} and V_{cz} of Fig. 7. Consequently, the range of V_{offset} for V_4 is represented as

$$\frac{V_{dc} T_{min}}{2T_s} < V_{offset} < \frac{V_{dc} T_{max}}{2T_s}, \quad (12)$$

Fig. 8 shows that V_2 is selected as V_{opt} . V_2 consists of

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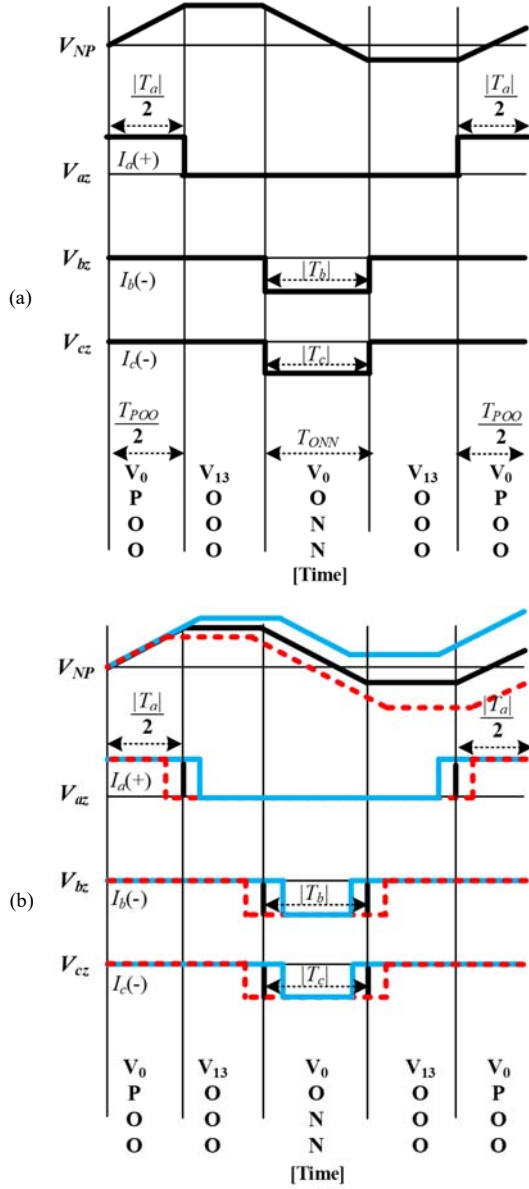


Fig. 7. V_{NP} and switching sequence of V_4 in Sector I. (a) Zero V_{offset} . (b) Positive V_{offset} (line) and negative V_{offset} (dotted-line).

three-switching states: POO, PON, and ONN. The analysis background for adding V_{offset} to V_2 is the same as the case of V_4 mentioned above. However, since making the T_{POO} and T_{ONN} to zero is determined by OFF-time ($[T_s - T_a]$, $[T_s - T_b]$, $[T_s - T_c]$), the range of V_{offset} is expressed as

$$-\frac{V_{dc}(T_s + T_{min})}{2T_s} < V_{offset} < \frac{V_{dc}(T_s - T_{max})}{2T_s}, \quad (13)$$

equation (13) can be applied when V_0 , V_1 , V_2 , or V_6 is V_{opt} .

When the V_3 or V_5 is determined as V_{opt} , the bi-directional V_{NP} variation is possible. In case of V_3 , there are two switching states: POO and OON. Among them, T_{POO} can be only decreased by the negative V_{offset} . If the positive V_{offset} is added, the range where Vienna rectifier operation rule is violated appears as shown in Fig. 9. Therefore, the range of V_{offset} for V_3 and V_5 in Sector I, III, V is represented as

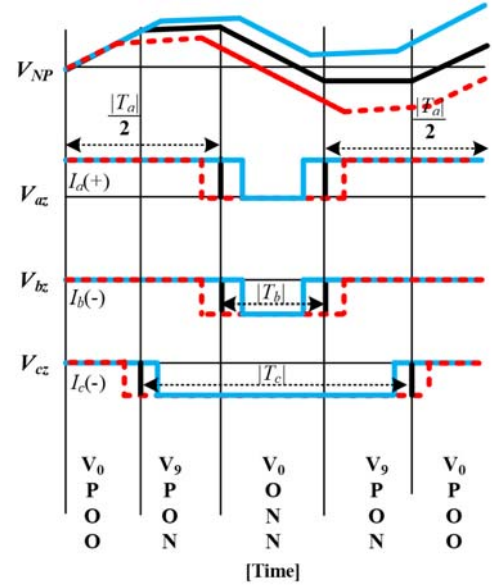


Fig. 8. V_{NP} and switching sequence of V_2 in Sector I: Positive V_{offset} (line) and negative V_{offset} (dotted-line).

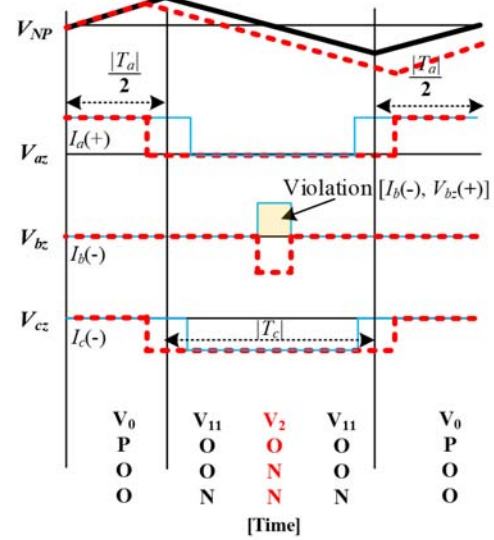


Fig. 9. V_{NP} and switching sequence of V_3 in Sector I: Positive V_{offset} (line, infeasible case) and negative V_{offset} (dotted-line).

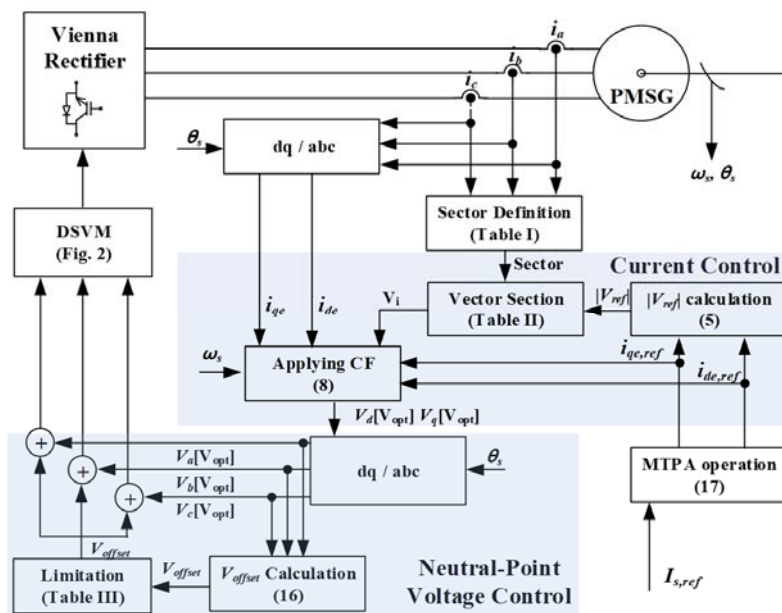
$$-\frac{V_{dc}(T_s + T_{min})}{2T_s} < V_{offset} < 0. \quad (14)$$

In cases of V_3 and V_5 , the bi-directional V_{NP} variation is determined by Sector. If Sector is odd as shown in Fig. 9, V_{NP} only decreases by adding the negative V_{offset} . On the other hand, the even Sector permits V_{NP} to be increased only when V_3 or V_5 is selected as V_{opt} by adding the positive V_{offset} . Therefore, in even Sector II, IV, VI, the range of V_{offset} is changed as

$$0 < V_{offset} < \frac{V_{dc}(T_s - T_{max})}{2}, \quad (15)$$

the summarized principle for the range of V_{offset} is shown in Table III.

In order to calculate the adjustable V_{offset} to make V_{NP} zero, the neutral-point voltage model of (4) is used. The equation with the V_{offset} making $V_{NP}[k+1]$ zero is represented as



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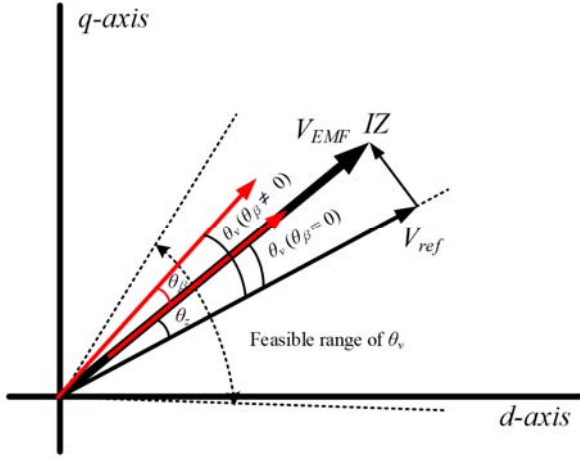


Fig. 11. Current and voltage waveforms of Vienna rectifier and IPMSG

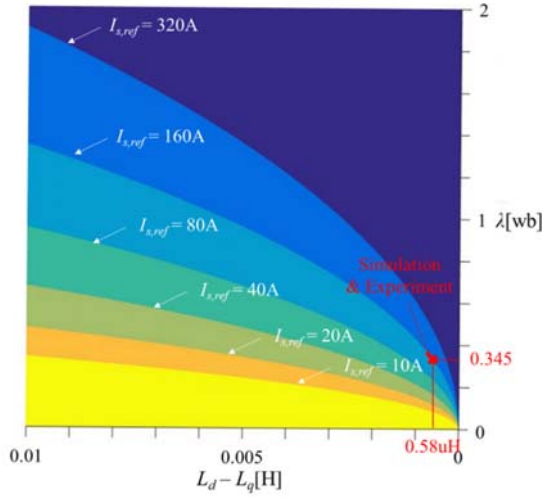


Fig. 12. The applicable parameters for MTPA control of IPMSG driven by Vienna rectifier depending on $I_{s,ref}$.

TABLE IV

IPMSG PARAMETERS

Number of pole	6
Rated voltage (line-to-line)	191 V _{rms}
Rated speed	1450 r/min
R_s	0.099 Ω
$L_{q,real}/L_{d,real}$	4.65 mH /4.07 mH

then, (18) is represented as

$$\sin(30^\circ - \theta_z) < \frac{-A + \sqrt{A^2 + 8B^2 I_s^2}}{4BI_s} \quad (20)$$

$$\theta_z = \left| \tan^{-1} \left(\frac{-R_s i_{de,ref} + \omega_s L_{qs} i_{qe,ref}}{-R_s i_{qe,ref} - \omega_s L_{ds} i_{de,ref} + \omega_s \lambda_m} \right) \right|,$$

where θ_z is calculated from (1). In Fig. 12, the range above each curve indicates the IPMSG parameters ($L_d - L_q$, λ) enabling the MTPA control in Vienna rectifier depending on I_s .

V. SIMULATION RESULTS

The simulation results to identify the performance of the FCS-MPC with DSVM are shown in this chapter. The simulation circuit is the same as Fig. 1 and the PMSG parameters of Table IV is used. The dc-link voltage is 300V, the

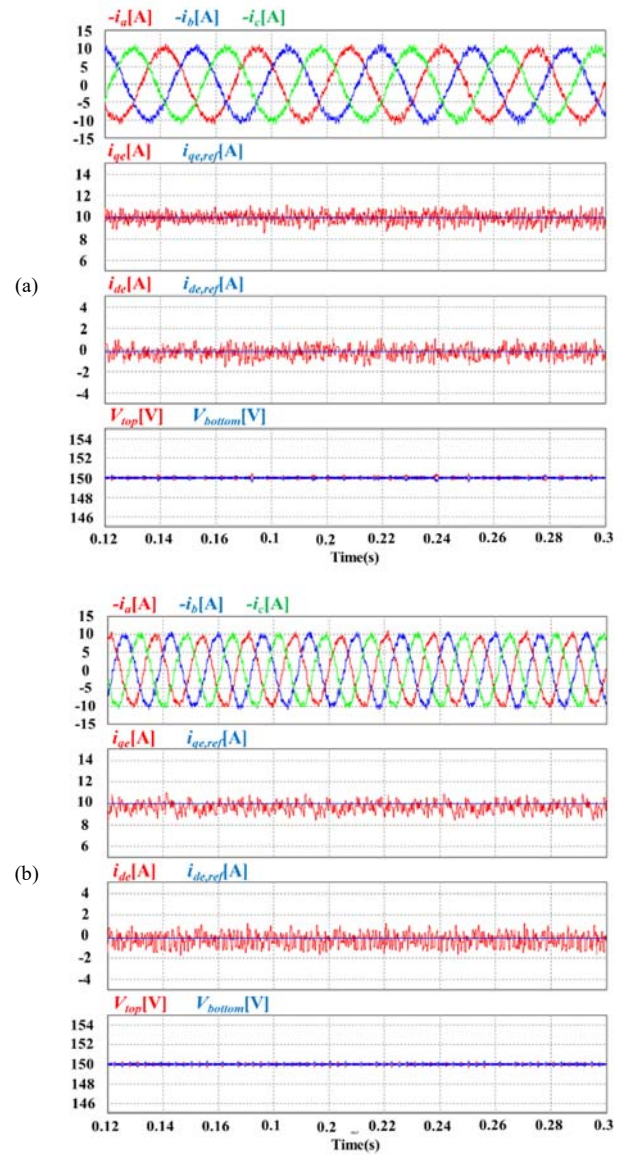


Fig. 13. Simulation results of the FCS-MPC with DSVM depending on the speed: $I_{s,ref}$ (10A). (a) Speed (600 r/m). (b) Speed (1200 r/m).

dc-link capacitor are 1100 μ F, and the sampling period (T_s) is 200 μ s. The $i_{de,ref}$ and $i_{qe,ref}$ are calculated by (17).

Fig. 13 shows the simulation results of the FCS-MPC with DSVM depending on the speed. Since $I_{s,ref}$ is set as 10 A, the $i_{qe,ref}$ and $i_{de,ref}$ are calculated as about 9.99 A and -0.17 A through the MTPA control. It is identified that the three leg currents are balanced and controlled as the desired value. Two dc-link voltages (V_{top} and V_{bottom}) have the same value which means that the neutral-point voltage balance is achieved.

Fig. 14 and Fig. 15 show key waveforms of the FCS-MPC with DSVM. When the speed is 600 r/m, $|V_{ref}|$ is located between $2V_{dc}/24$ and $6V_{dc}/24$ as shown in Fig. 14(a). Therefore, only 8 voltage vectors (V_0 , V_3 , V_4 , V_5 , V_{12} , V_{13} , V_{14} , V_{15}) becomes the candidate vector. In addition, it can be identified that the one vector of the 8 voltage vectors is determined as V_{opt} through the third waveform which shows the voltage vector number selected in Fig. 14(a). In Fig. 14(b), V_{offset} is calculated

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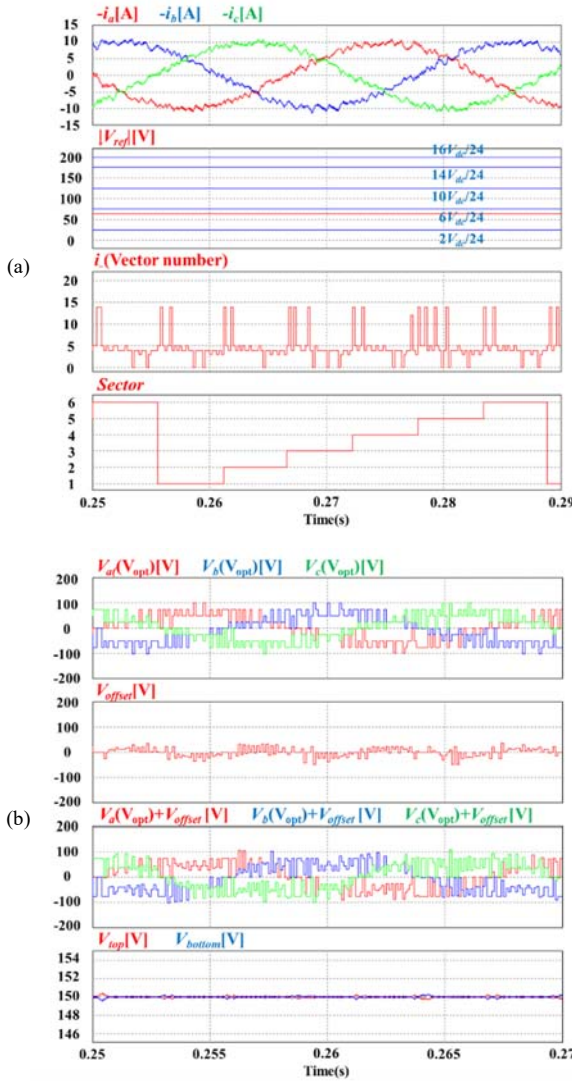


Fig. 14. Key waveforms of the FCS-MPC with DSVM when the speed is 600 r/m: $I_{s,ref}$ (10A). (a) Related to the neutral-point voltage control. (b) Related to the current control.

and influences on the two dc-link voltages (V_{top} , V_{bottom}). Although the calculated V_{offset} is added to $V_x[V_{opt}]$, the region where the voltage difference between V_{top} and V_{bottom} is not almost zero appears periodically as shown in last one of Fig. 14(b). It is because that only several voltage vectors can accept V_{offset} ; furthermore, V_{offset} is limited to value defined in Table III depending on the selected voltage vector. The evidences at 1200 r/m which are the same as Fig. 14 are shown in Fig. 15.

Fig. 16 shows the neutral-point voltage control ability of the FCS-MPC with DSVM. The neutral-point voltage unbalance is generated by connecting the resistor to the top side capacitor (C_{bottom}) in parallel. For the neutral-point voltage control, V_{offset} calculated from (16) is applied after 0.25s. It is shown that the neutral-point voltage becomes almost zero by V_{offset} in Fig. 16.

In Fig. 17, the θ_z and θ_β are changed as $I_{s,ref}$ incensement. In case of $I_{s,ref}$ (10A), the θ_z and θ_β are 7.8° and 1.0° respectively and the summation of them does not exceed 30° which is the operation limitation angle for Vienna Rectifier. After $I_{s,ref}$ is changed to 20 A, the summation is under 30° A even though

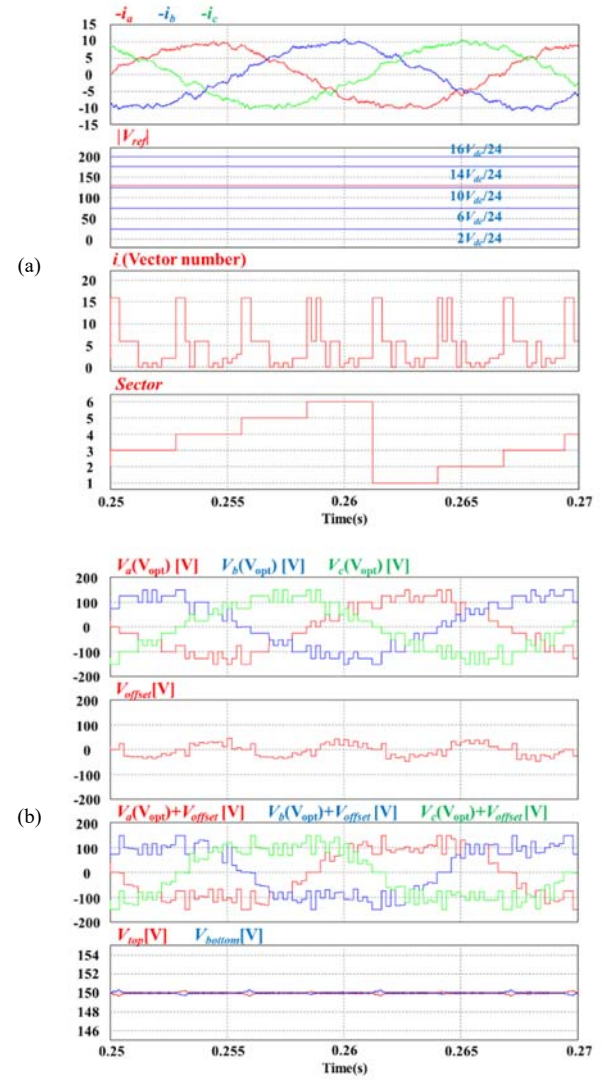


Fig. 15. Key waveforms of the FCS-MPC with DSVM when the speed is 1200 r/m: $I_{s,ref}$ (10A). (a) Related to the neutral-point voltage control. (b) Related to the current control.

two values increase. In addition, the incensement of output torque ($Torque_{avg}$) can be shown when the MTPA control is applied during 0.2s ~ 0.3s compared to when $I_{de,ref}$ is 0 during 0.3s ~ 0.4s.

The simulation result of the FCS-MPC with DSVM under the speed variation of PMSG is shown in Fig. 18. $I_{s,ref}$ set as 10 A during the speed variation. Although the speed of PMSG increases, i_{qe} and i_{de} are controlled as $I_{de,ref}$ and $I_{qe,ref}$ beside the neutral-point voltage is maintained as almost zero which means that two dc-link voltages (V_{top} , V_{bottom}) have the same value.

Fig. 19 and 20 shows the key waveforms when the parameter is changed and the comparison results between the proposed FCS-MPC with DSVM and the conventional MPC [4] of Vienna rectifier at the same condition. In determining the control principle of the conventional MPC, the band of neutral-point voltage control is 2 V and weighting factor constant for reducing the number of the switching is zero to improve the current quality maximally. Although the control period of two methods is 200 us, the proposed method has the

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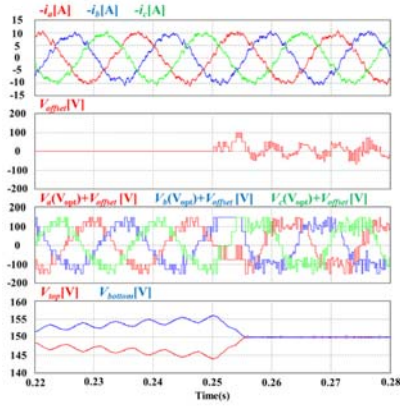


Fig. 16. Simulation results of the FCS-MPC with DSVM for identifying the neutral-point voltage control: 1200 r/m.

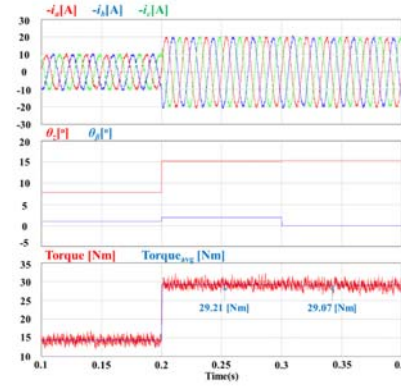


Fig. 17. Simulation results of the FCS-MPC with DSVM when $I_{s,ref}$ increases with and without MTPA: 1200 r/m.

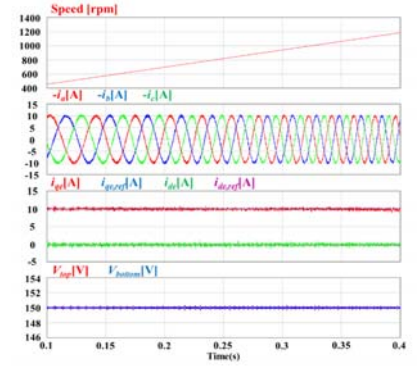
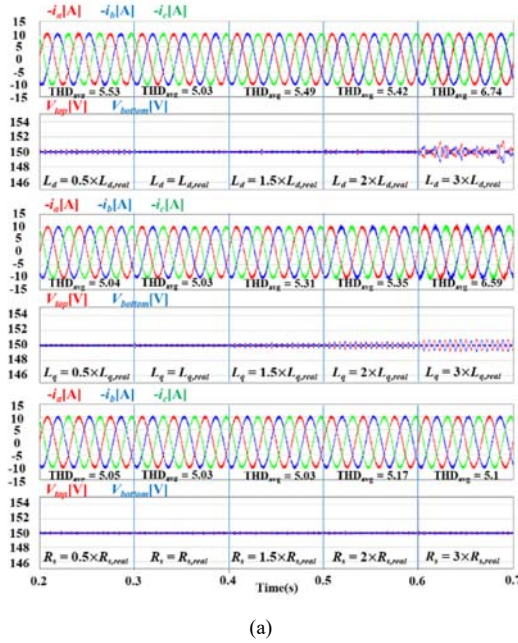
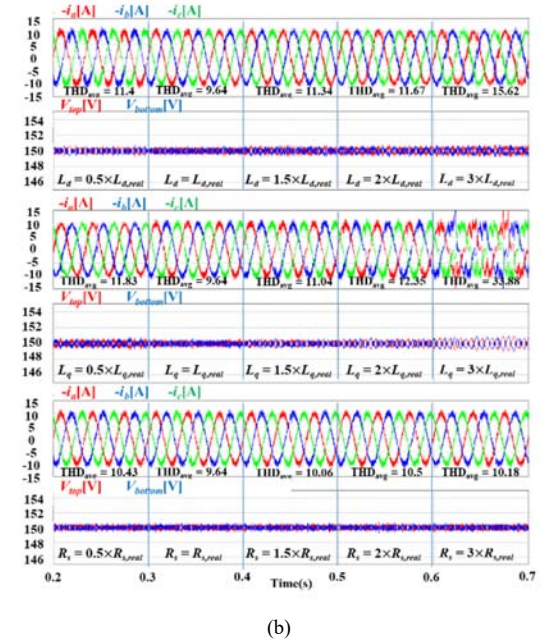


Fig. 18. Simulation results of the FCS-MPC with DSVM under the speed variation of PMSG.



(a)



(b)

Fig. 19. Comparison results for parameter (L_d , L_q and R_s) variation at $I_{s,ref}$ (10A) and speed (600 r/m). (a) The FCS-MPC with DSVM. (b) The conventional MPC [4].

lower total harmonic distortion (THD) of current than that of the conventional MPC in all results because the proposed method combines the MPC with the DSVM. Two methods show the similar tendency that the current quality and the neutral-point voltage ripple are aggravated as the L_d or L_q changes dramatically as shown in Fig. 19. In case of $3 \times L_{d,real}$ and $3 \times L_{q,real}$, however, the FCS-MPC with DSVM show the robustness more than the conventional MPC. On the other hand, the R_s variation does not influence on the performance of two methods. The one of reason is that the PMSG used in this paper has the small R_s to be negligible.

Although the FCS-MPC with DSVM has the robustness for L_d or L_q variation more than the conventional MPC, the large variation of λ_m is fatal for the FCS-MPC with DSVM as shown in Fig. 20. Since λ_m is the dominant factor for the $|V_{ref}|$ calculation in Step 1. Voltage vector selection, the large variation of λ_m leads to select the wrong voltage vectors of the undesired level in Table II and it make the current quality and

neutral-point voltage ripple serious more than the conventional MPC.

VI. EXPERIMENTAL RESULTS

The proposed FCS-MPC with DSVM for Vienna rectifiers was proved in experiment of three-level rectifier as shown in Fig. 21. The outer six switches of three-level rectifier are turn-off to operate as Vienna rectifier. The TMS320F28335 was used to control the experimental setup. The parameters in the simulation are used in the experiment

Fig. 22(a) and (b) show the comparison results between the proposed FCS-MPC with DSVM and the FCS-MPC with DSVM using all voltage vectors. The level showing $|V_{ref}|$ is 2 in Fig. 22(a) and the number of the candidate vector is twelve which is smaller than 19 (the number of all voltage vectors). Nevertheless, the effective voltage vector selection of the proposed FCS-MPC with DSVM guarantees the same current quality with the that of the FCS-MPC with DSVM using all

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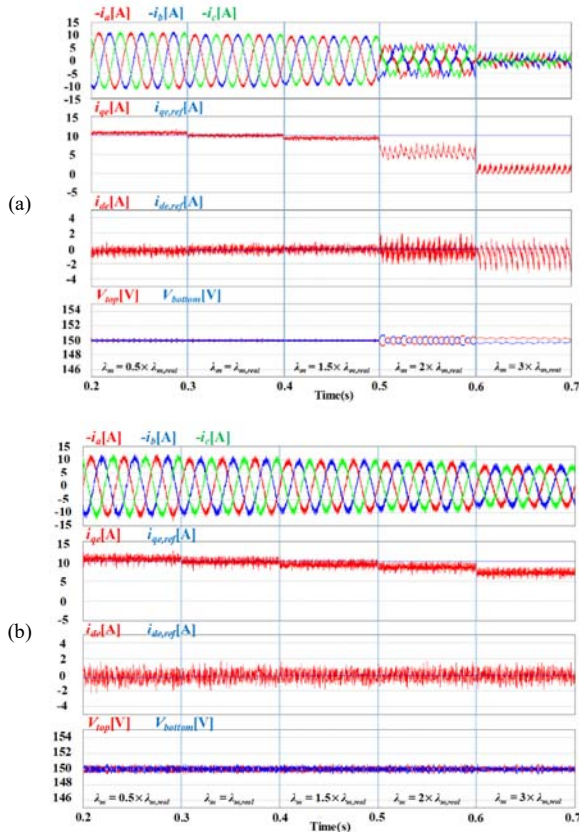


Fig. 20. Comparison results for parameter (λ_m) variation at $I_{s,ref}$ (10A) and speed (600 r/m). (a) The FCS-MPC with DSVM. (b) The conventional MPC [4].

vectors. In addition, the FCS-MPC with DSVM using all vectors aggravates rather the current quality as shown in dotted line of Fig. 22(b). It is because the current sampling noise makes the error in the optimal voltage selection. At high speed of Fig. 22(c), the proposed FCS-MPC with DSVM shows the same result of the simulation results.

The neutral-point voltage balancing results of the proposed FCS-MPC with DSVM are shown in Fig. 23. In both figures, V_{offset} is generated and it added to $V_x(V_{opt})$ to make the voltage difference between V_{top} and V_{bottom} zero. In addition, the waveforms of experimental results are the same as those of simulation results.

The calculation time of the proposed FCS-MPC with DSVM in TMS320F28335 was measured and compared to that of the FCS-MPC with DSVM using all voltage vectors. The results of the measurement depending on the level showing $|V_{ref}|$ are shown in Table V. In case of Level 0, the minimum time is shown because the minimum voltage vectors (four) are considered as the candidate vector. The level 3 has the maximum time owing to the largest number of candidate vectors. The proposed FCS-MPC with DSVM reduces the 26.8% of calculation burden averagely.

The proposed FCS-MPC with DSVM has the fast dynamic response as shown in Fig. 24. It is one of the characteristics that the FCS-MPC has. In addition, by increasing $I_{s,ref}$ the $\theta_s + \theta_\beta$ is changed from 4° to 8° . it does not exceed 30° which is the operation limitation angle for Vienna Rectifier.

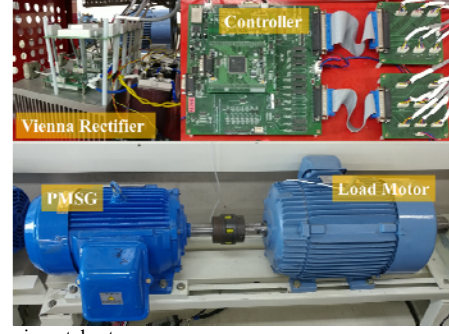


Fig. 21. Experimental setup.

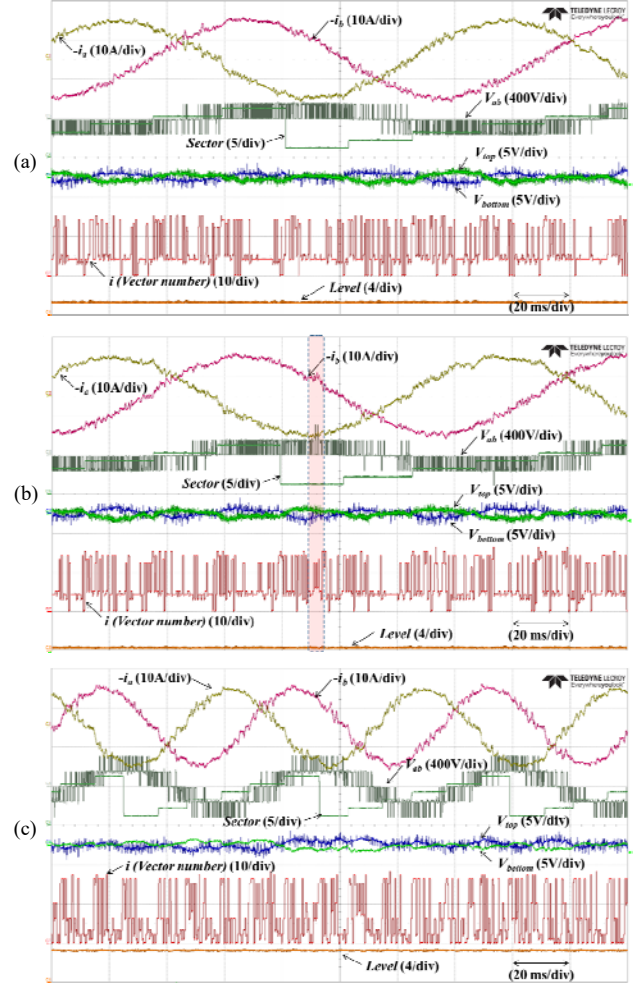


Fig. 22. Comparison results between the proposed FCS-MPC with DSVM and FCS-MPC with DSVM using all voltage vectors: $I_{s,ref}$ (10A). (a) The proposed FCS-MPC with DSVM (600r/m). (b) FCS-MPC with DSVM using all voltage vectors (600r/m). (c) The proposed FCS-MPC with DSVM (1200r/m).

VII. CONCLUSION

In this paper, a finite control set-MPC (FCS-MPC) with the discrete space-vector modulation (DSVM) of Vienna rectifier for the permanent magnet synchronous generators (PMSGs) of wind turbine systems (WTSS) is proposed. The proposed FCS-MPC with DSVM effectively selects the candidate vectors for the cost function depending on the magnitude of the reference voltage calculated through the PMSG model, and it leads to the reduction of calculation burden. Furthermore, the

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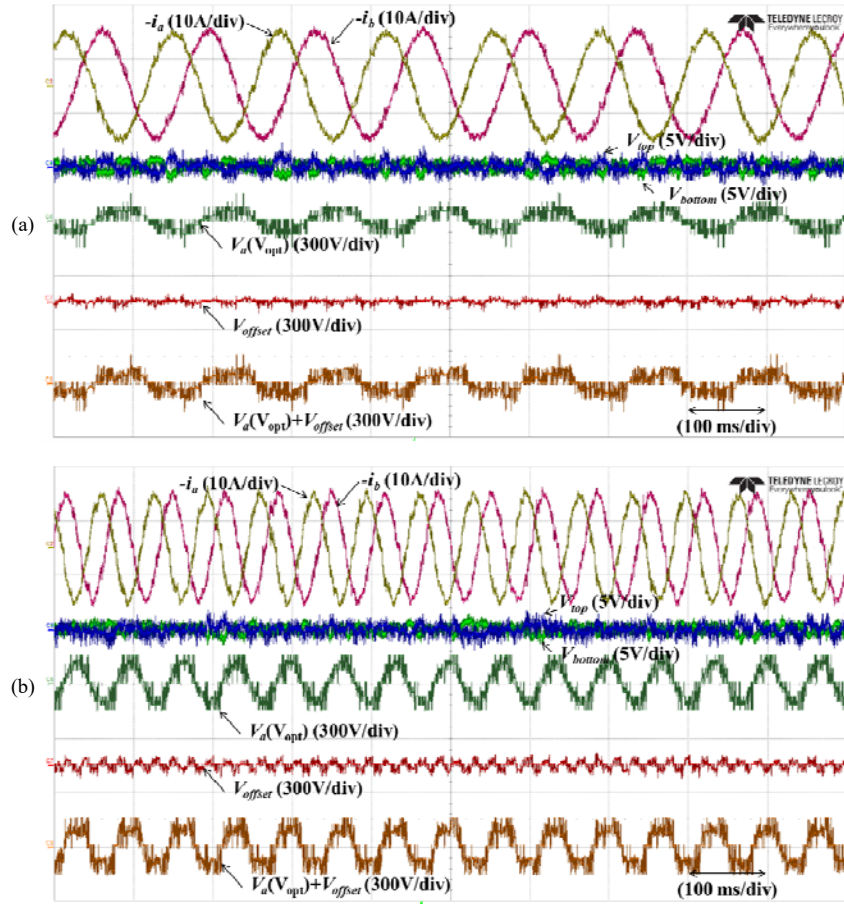


Fig. 23. Experimental results of the neutral-point voltage balancing of the proposed FCS-MPC with DSVM depending on the speed: $I_{s,ref}$ (10A). (a) Speed (600r/m). (b) Speed (1200r/m).

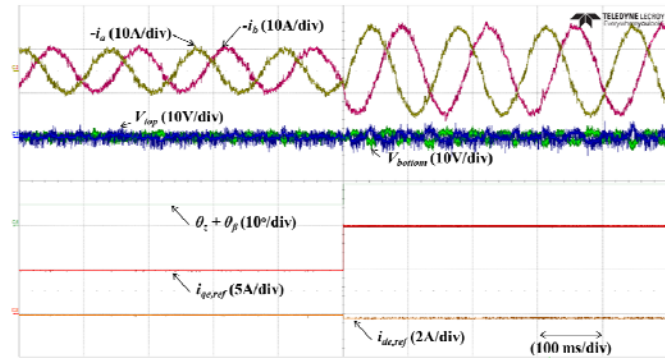


Fig. 24. Experimental results of the proposed FCS-MPC with DSVM when $I_{s,ref}$ increases 5A to 10A: 600 r/m.

DSVM causes that the current ripple component is mitigated and the neutral-point voltage is controlled as balance by the offset voltage calculated through the neutral-point voltage model of Vienna rectifier. Therefore, the proposed FCS-MPC with DSVM achieves the high performance of both the low current ripple and fast dynamic response. The performance and feasibility of the proposed FCS-MPC with DSVM are proved through the simulation and experimental results. In addition, the acceptable parameters of PMSG for applying the maximum torque per ampere (MTPA) control in Vienna rectifier are analyzed and it means that this paper suggests the guideline for

TABLE V COMPARISON RESULTS IN TERMS OF CALCULATION BURDEN		
Level	The proposed FCS-MPC with DSVM	FCS-MPC with DSVM using all vectors
0	23.5 us (36% reduction)	37 us
1	26 us (30% reduction)	
2	28 us (24% reduction)	
3	30.5 us (18% reduction)	
4	27.5 us (26% reduction)	

using PSMG with Vienna rectifier. Since the assumption which is the speed and angle of PMSG do not changed during a control (sampling) period is considered in this paper, the control (sampling) period should be small enough than the time constant of PMSG speed to apply the proposed method.

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